

1/10

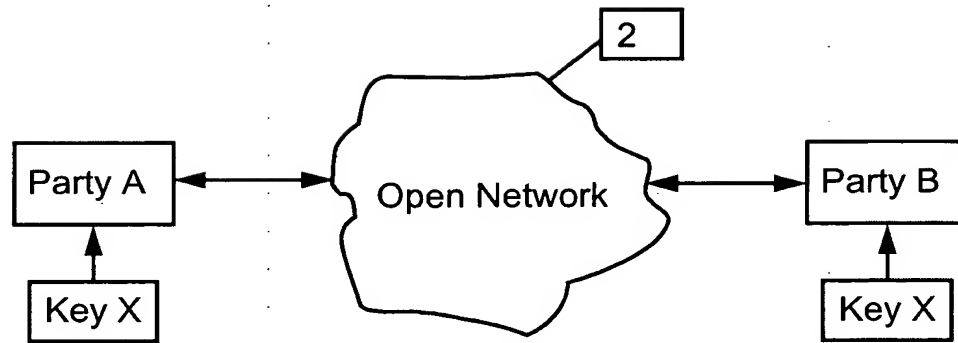


Fig. 1

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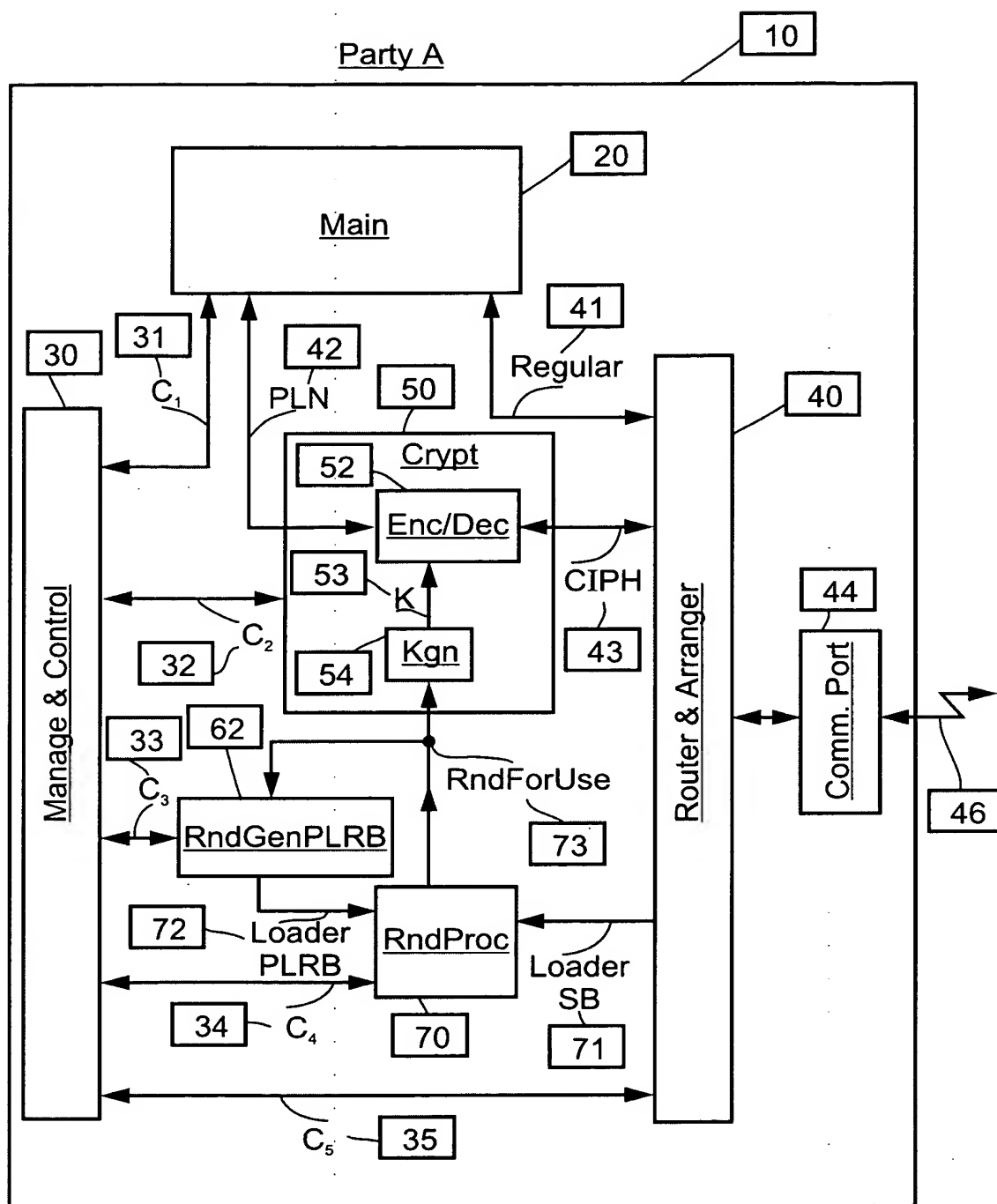


Fig. 2

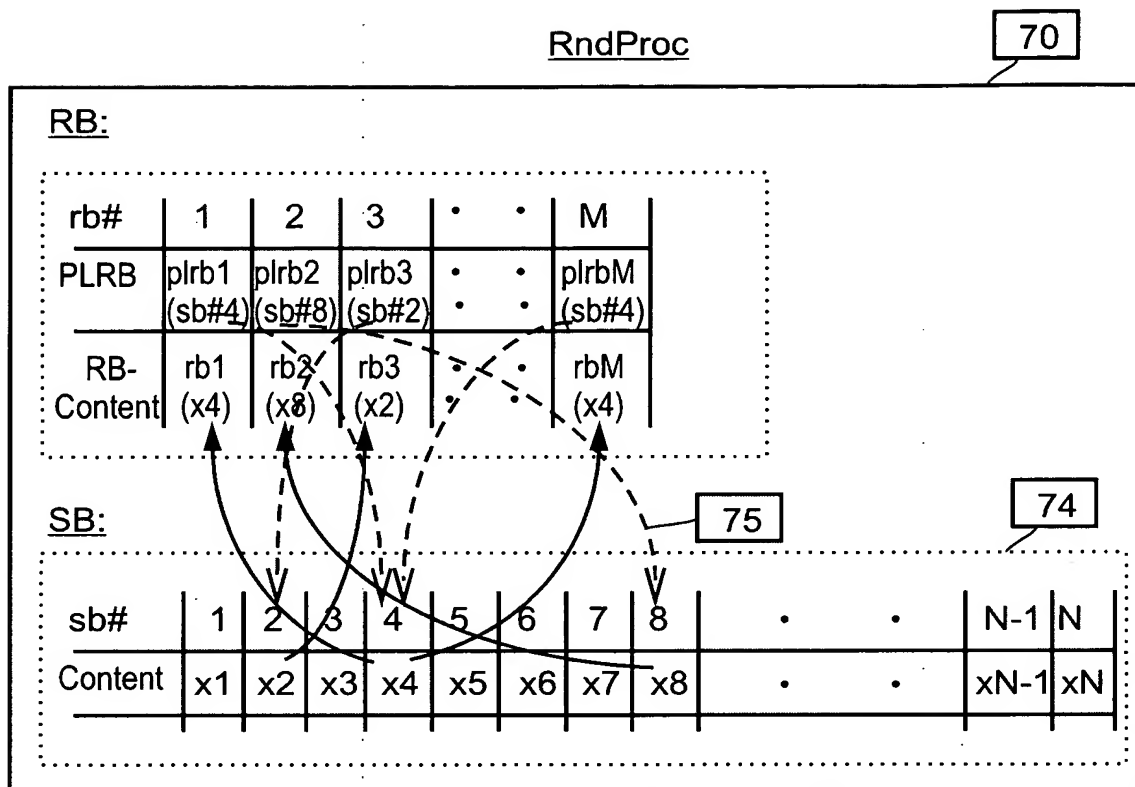


Fig. 3

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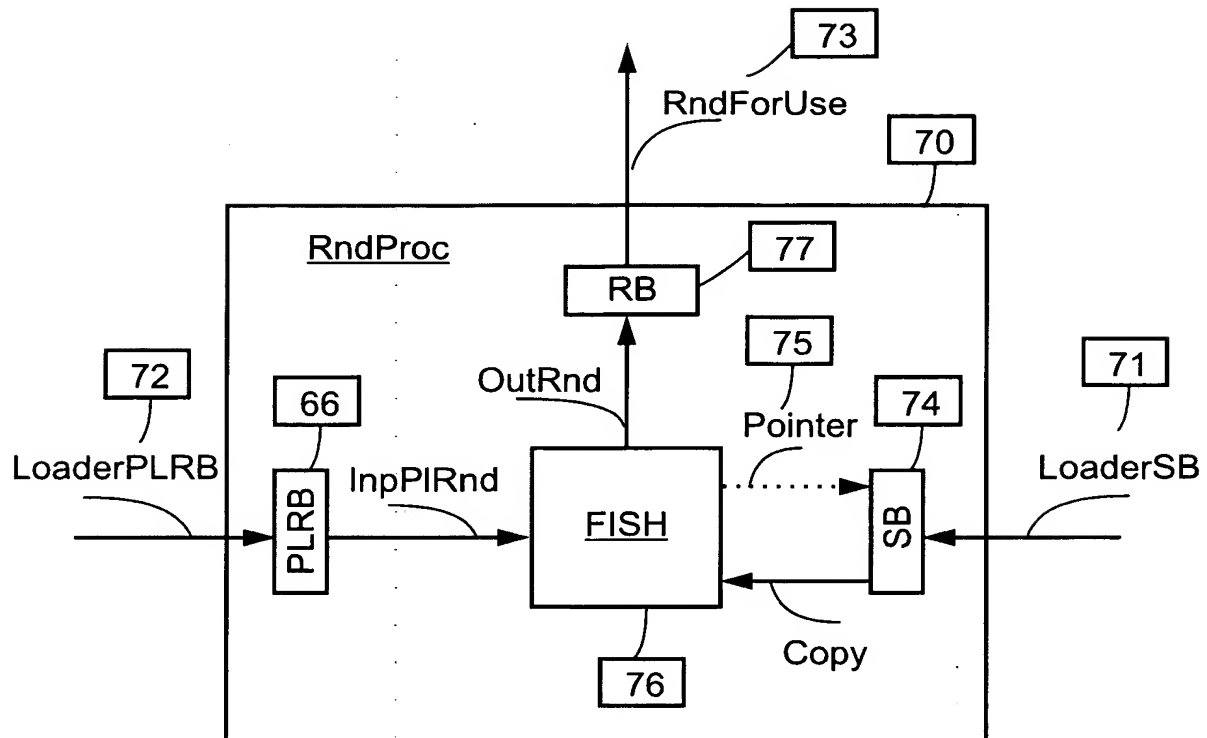
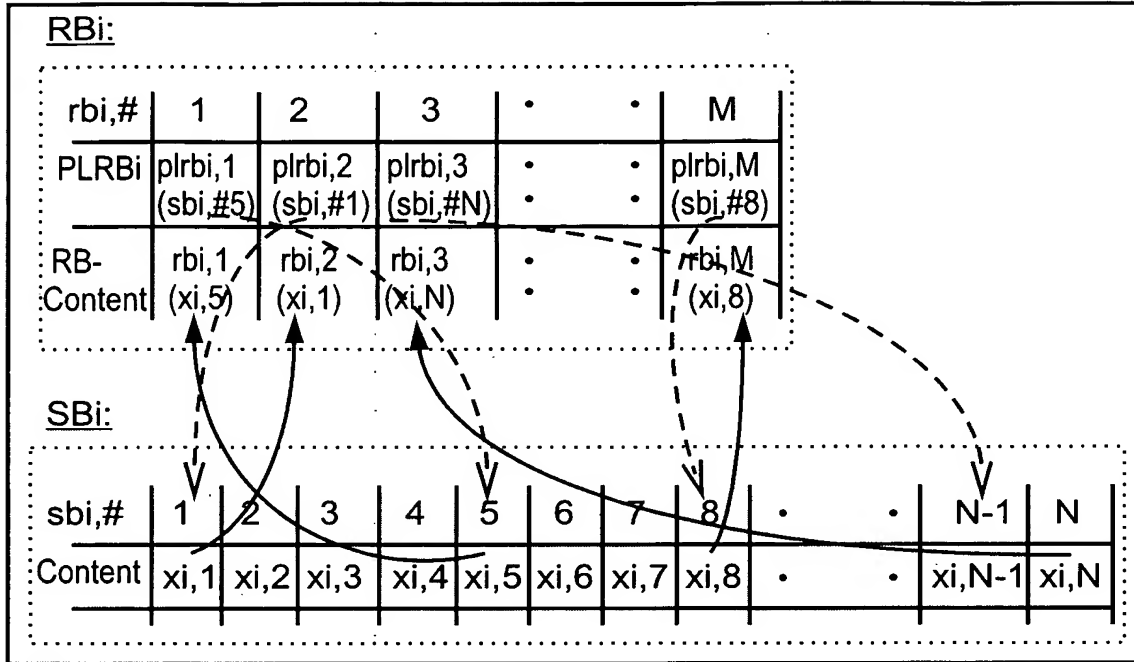


Fig. 4

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RndProci



RndProci+1

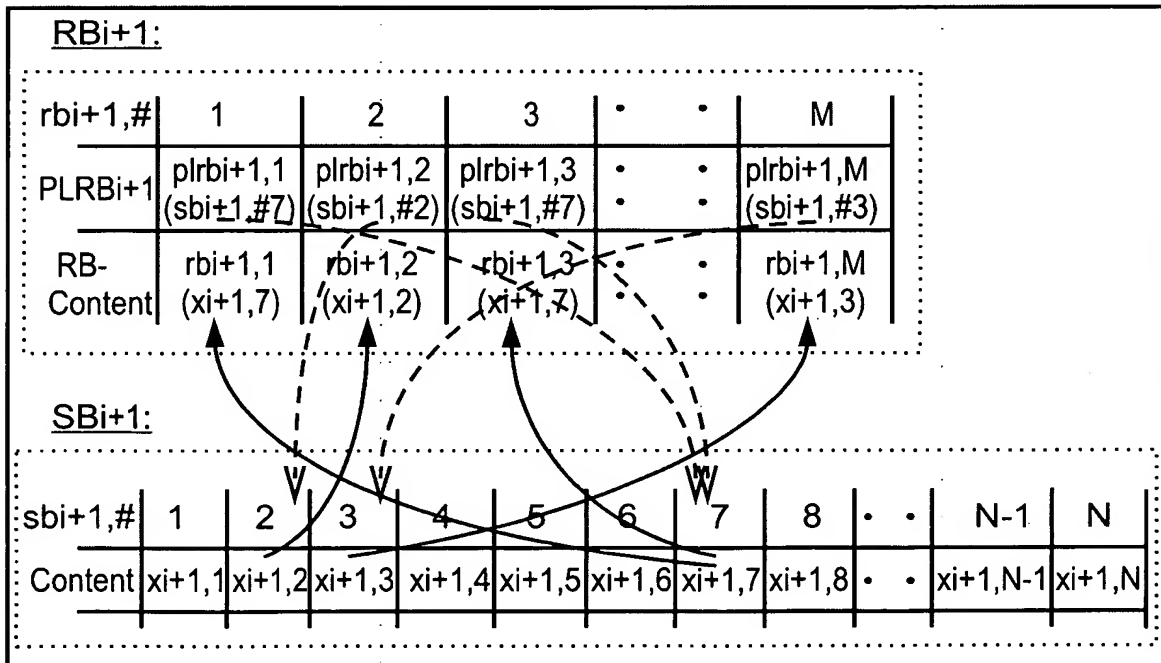


Fig. 5

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10

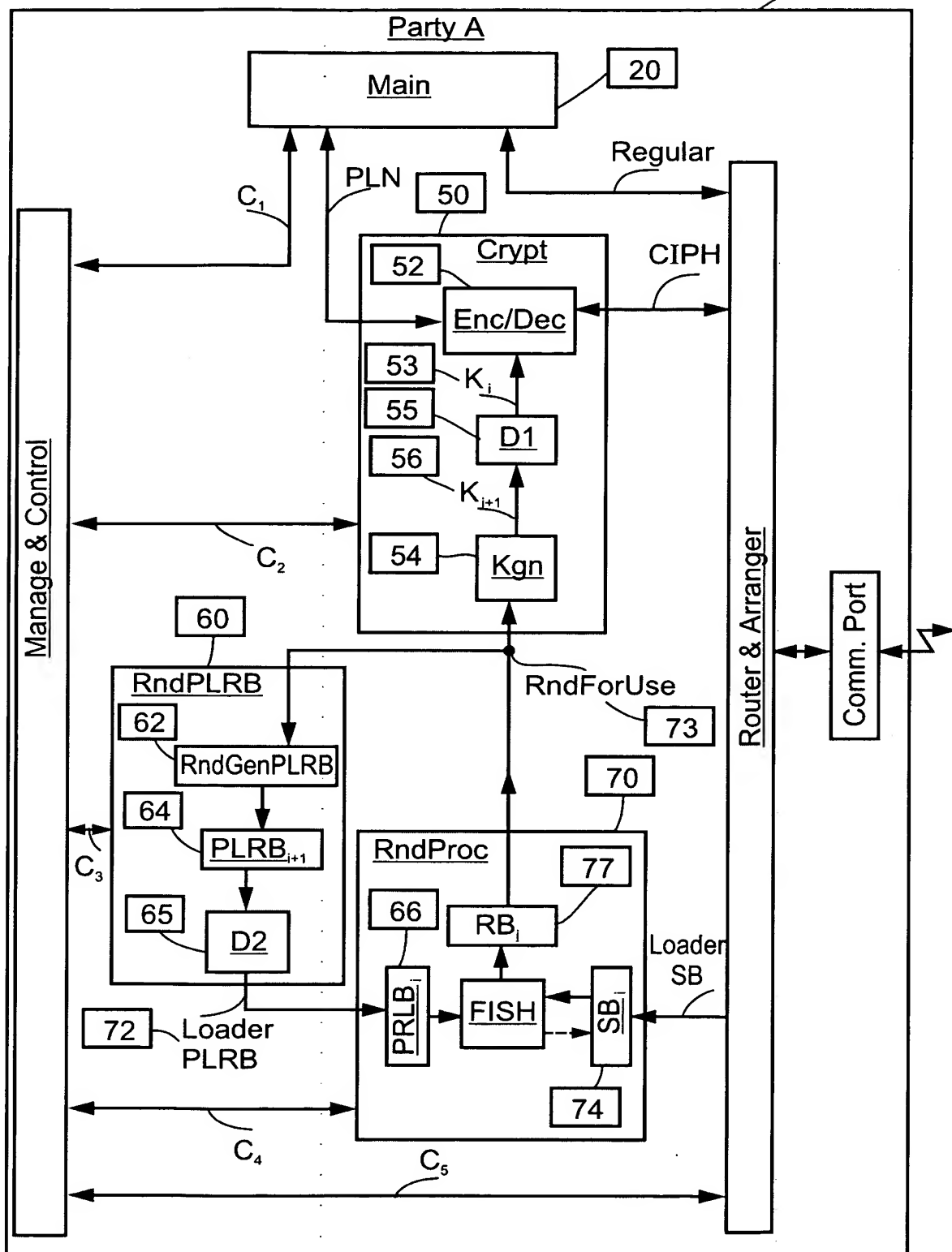


Fig. 6

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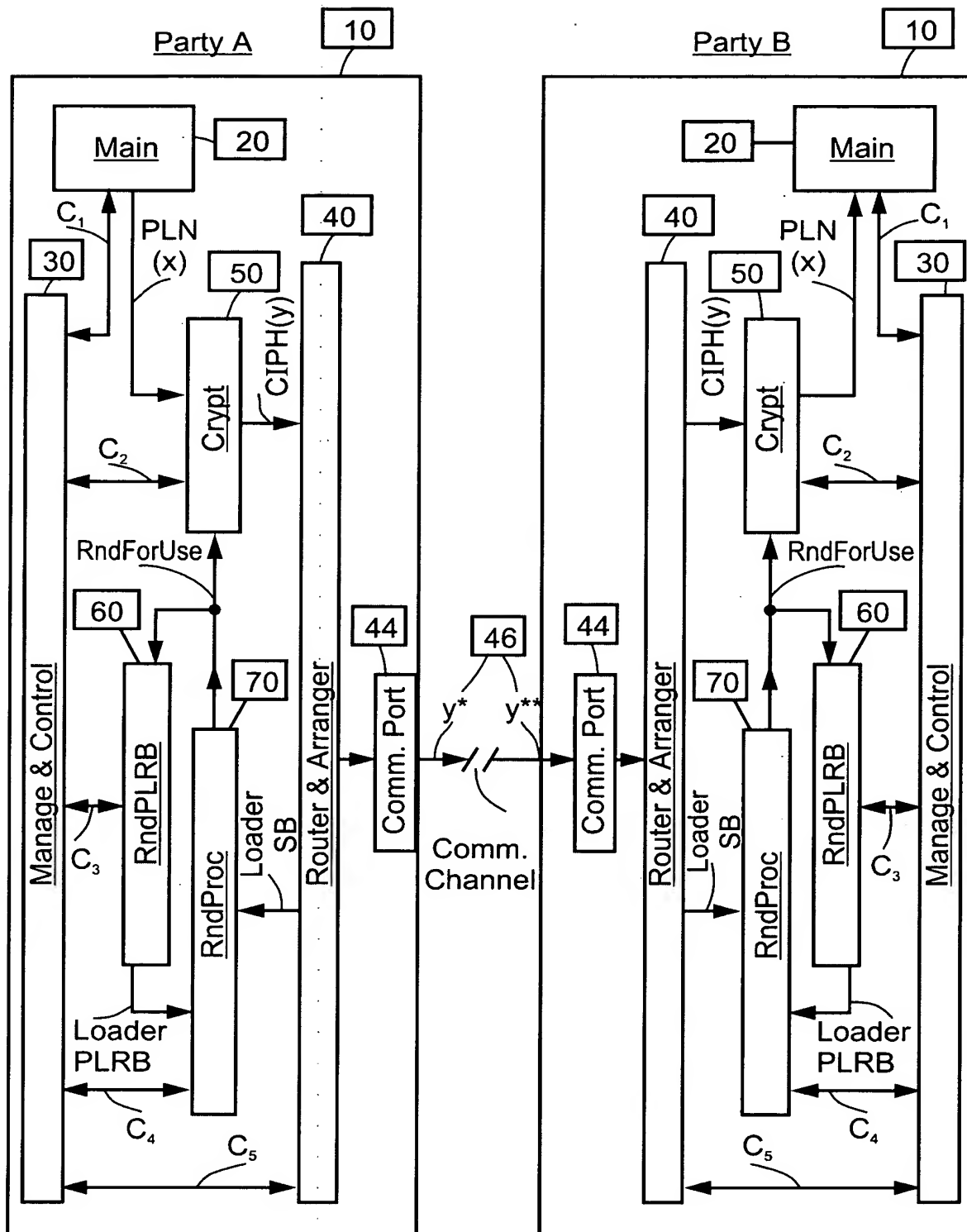


Fig. 7

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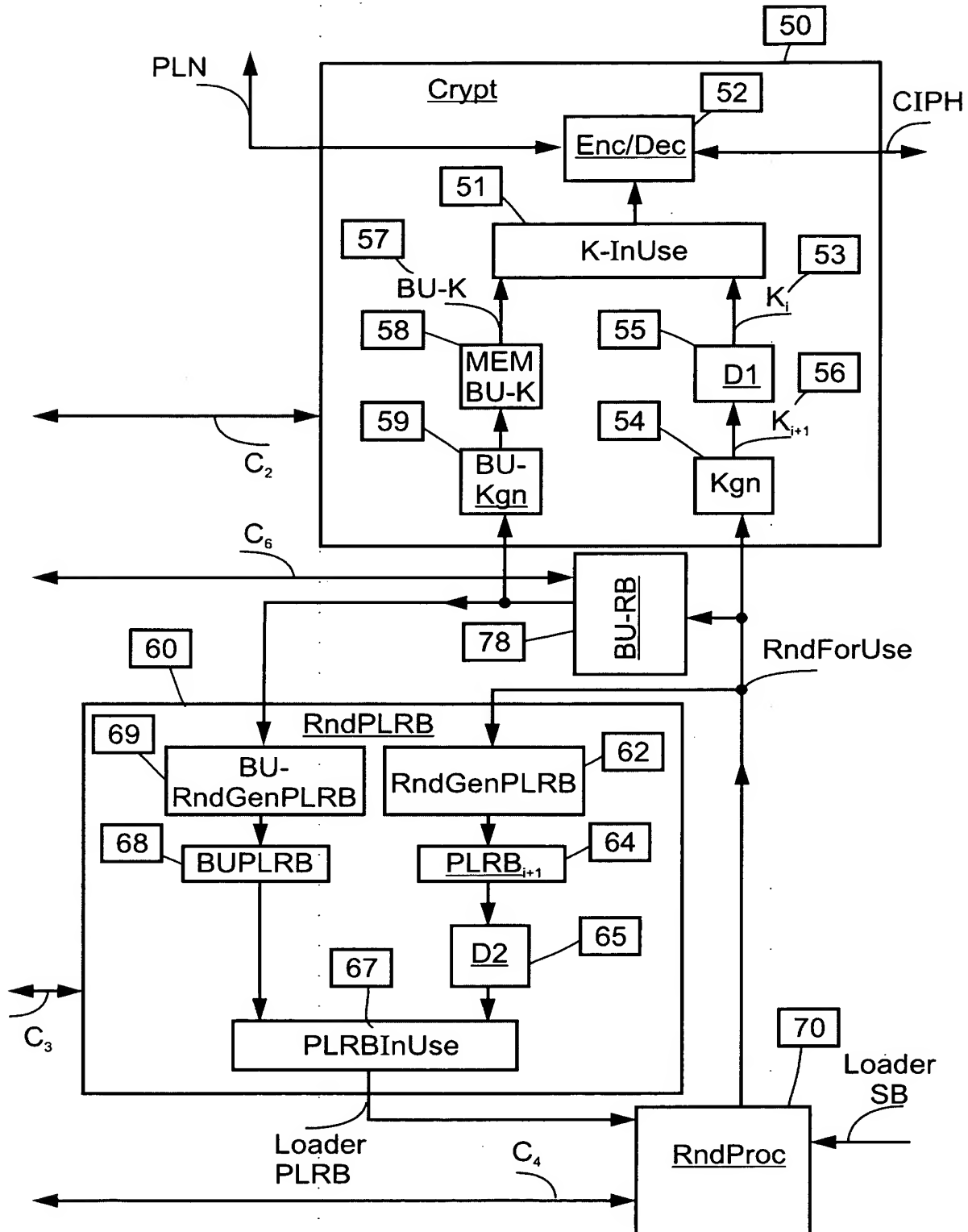


Fig. 8

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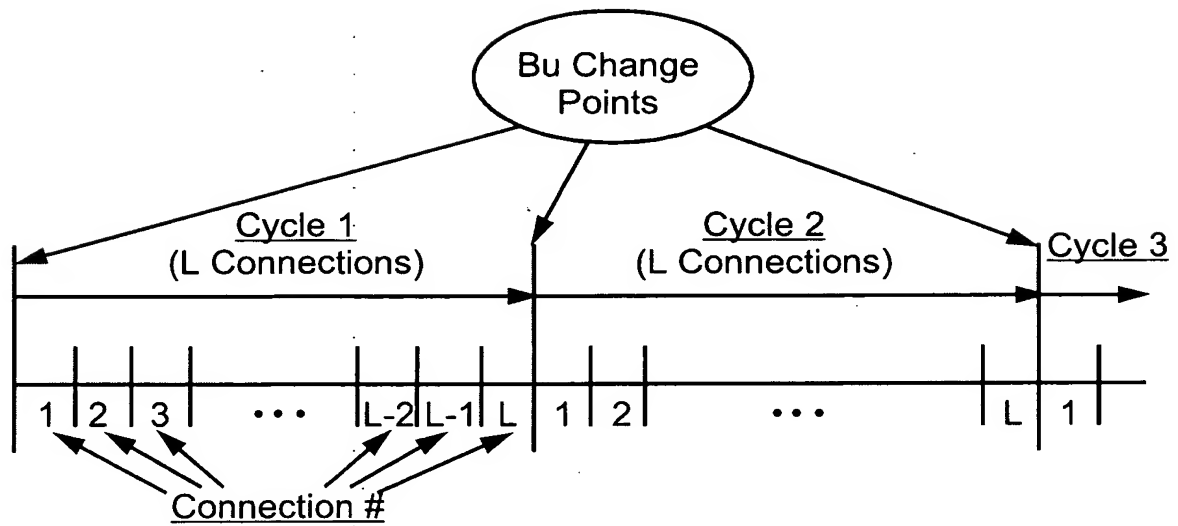


Fig. 9

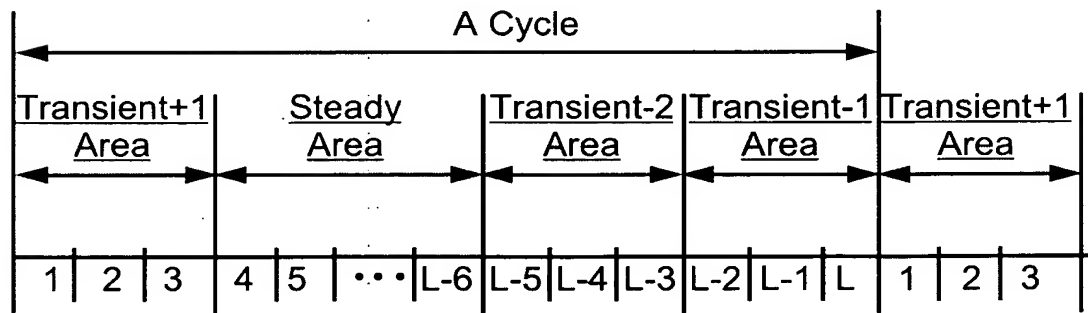


Fig. 10

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| Area | BU in Memory | Transmitter | | Receiver | |
|--------------------|-----------------|----------------|--------|----------------|--------|
| | | Control Snt | BU Use | Control Rcv | BU Use |
| <u>Steady</u> | old | 'Old' | 'old' | Ignore | 'old' |
| <u>Transient-2</u> | old | 'Old' | 'old' | 'Old' | 'old' |
| | | | | 'New' | 'new1' |
| <u>Transient-1</u> | old | 'New' | 'new1' | 'Old' | 'old' |
| | | | | 'New' | 'new1' |
| <u>Transient+1</u> | new | 'New' | 'new' | Ignore | 'new' |

Fig. 11